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10550326 - GAI: 2439

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	Filing Date		2005-09-23	
	First Named Inventor	Roger D. Chamberlain		
	Art Unit	2439		
	Examiner Name	Tolentino, Roderick		
	Attorney Docket Number	53047-57365		

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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

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/R.T./	1	Google Search Results Page for "field programmable gate array financial calculation stock market" over dates of January 1, 1990-May 21, 2002, 1 page	<input type="checkbox"/>
/R.T./	2	Office Action for US Application 11/932596 dated December 3, 2010	<input type="checkbox"/>
/R.T./	3	Sachin Tandon, "A Programmable Architecture for Real-Time Derivative Trading", Master's Thesis, University of Edinburgh, 2003, Published in Google on Jan. 1, 2001	<input type="checkbox"/>

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Examiner Signature

/Roderick Tolentino/ (12/13/2010)

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- ☐ See attached certification statement.
- ☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

### SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Benjamin L. Volk, Jr./	Date (YYYY-MM-DD)	2010-12-11
Name/Print	Benjamin L. Volk, Jr.	Registration Number	48017

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